
Optical PMD Sub Task Force Report

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Key Issues

- Emphasis on achieving technical completeness – filling in missing values and removing inconsistencies.
- C58: Identifying FEC implications. Clocking architecture and jitter transfer.
- C59: TDP details.
- C60: Test patterns. Adapting jitter values for single-sided clock recovery. BER extrapolation and testability.
- Common to all clauses: Frame based testing. Refining Jitter measurement methods and values.