

8/20/26		Sep 14	Sep 15	Sep 16	Sep 17
		Monday	Tuesday	Wednesday	Thursday
Large Berlin	AM1	DJ	DS	400G	400G
	AM2	DJ	DS	400G	400G
	PM1	DJ	400G	400G	400G
	PM2	DJ	400G	400G	400G
	Evening				WG
Medium Los Angeles	AM1	EPLCA	EPLCA	EPLCA	DM
	AM2	EPLCA	EPLCA	EPLCA	DM
	PM1	EFMP	DM	DM	DQ
	PM2	EFMP	DM	DM	DQ
	Evening				
Small Munich	AM1	DJ Logic			
	AM2	DJ Logic			
	PM1				
	PM2				
	Evening				

Timeslots: AM1 AM2 PM1 PM2 Evening
 8:00-10:00 10:15-12:15 1:15-3:15 3:30-6:00 7:00-9:00

Decoder:

DJ IEEE P802.3dj 200 Gb/s, 400 Gb/s, 800 Gb/s, and 1.6 Tb/s Ethernet Task Force
 DJE DJ electrical track
 DJO DJ optics track
 DJL DJ logic track
 DM IEEE P802.3dm Ethernet for Automotive Imaging Sensors Task Force
 DP IEEE P802.3dp Single-Pair Ethernet Powering Cabling Restrictions Task Force
 DQ IEEE 802.3dq Pin-Optimized PHY Interface Task Force
 DR IEEE P802.3dr P802.3-2022/Cor 2
 DS IEEE P802.3ds 200 Gb/s per Wavelength MMF PHYs Task Force
 DT IEEE 802.3dt Ethernet Metadata Services Task Force
 400G IEEE 802.3 400 Gb/s/Lane Signaling Study Group (DV)
 EFMP IEEE 802.3 FMP Ethernet Interoperability Study Group (DW)
 Maintenance IEEE 802.3 Maintenance
 WG IEEE 802.3 Ethernet Working Group meeting
 NEA IEEE 803.2 New Ethernet Applications Ad Hoc
 PDCC IEEE 802.3 Power Delivery Coordinating Committee (PDCC) Ad Hoc
 YANG YANG Open Source Project Ad Hoc
 COM Channel Operating Margin (COM) Open Source Project Ad Hoc
 EPLCA