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| REVISION REQUEST |
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DATE: May 14th, 2024
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REQUESTED REVISION:
  STANDARD: 802.3cx / 802.3df
  CLAUSE NUMBER: 90A.3
  CLAUSE TITLE: Considerations for use of different data delay
  measurement points
PROPOSED REVISION TEXT:
  In Table 90A–1—Magnitude of potential timestamp accuracy impairments,
  the magnitude of potential timestamp accuracy impairments due to
  alignment marker / codeword marker insertion/removal for 200G,
  400G and 800G should all be 5.12

RATIONALE FOR REVISION:
  The current number in the table for 200G/400G/800G, 2.56, is wrong.
  The time in the table is meant to the the AM's time on the wire,
  or MII time of the alignment markers.
  The alignment markers for 200/400G/800G are 4/8/16x 257b blocks,
  which is equivalent to 1024/2048/4096 MII bits, which is 5.12ns
  for all 3 rates.

IMPACT ON EXISTING NETWORKS:
  No impact on existing networks, just reflects the true number.
  Annex 90A is only informative, not normative.
  The 'potential' timestamp impairment is only relevant if either end
  of the link do not support the 'PCS Dynamic Path Data Delay ability'
  (MDIO bit 3.1800.10)

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|Please attach supporting material, if any |
|Submit to:-   David Law, Chair IEEE 802.3 |
|and copy:-   Adam Healey, Vice-Chair IEEE 802.3 |
| |
|At:-   E-Mail: stds-802-3-maint-req@ieee.org |
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| +------------ For official use ------------+ |
	REV REQ NUMBER: 1432	
	DATE RECEIVED: 14 May 2024	
	EDITORIAL/TECHNICAL	
	ACCEPTED/DENIED	
	BALLOT REQ'D YES/NO	
	COMMENTS:	
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| For information about this Revision Request see - |
|http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1432 |
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