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4  | DATE: 04 October 2024              |
5  | NAME: Charity Reed                  |
6  | COMPANY/AFFILIATION: UNH-IOL        |
7  | E-MAIL: creed@iol.unh.edu          |
8  |                                     |
9  | REQUESTED REVISION:                 |
10 |   STANDARD: 802.3-2022               |
11 |   CLAUSE NUMBER: 149.3.9.2.1        |
12 |   CLAUSE TITLE: MultiGBASE-T1 OAM frame structure |
13 | PROPOSED REVISION TEXT:             |
14 | Replace                               |
15 |                                     |
16 | "One OAM symbol is placed in the 10-bit OAM field in each PHY frame      |
17 | during normal power operation in the data mode. One OAM symbol is        |
18 | placed in the 10-bit OAM field in each refresh cycle during Low Power     |
19 | Idle. The sixteen OAM symbols are consecutively inserted into sixteen    |
20 | consecutive PHY frames and/or refresh cycles."                             |
21 |                                     |
22 | with                                  |
23 |                                     |
24 | "One OAM symbol is placed in the 10-bit OAM field in each RS-FEC frame    |
25 | during normal power operation in the data mode. One OAM symbol is        |
26 | placed in the 10-bit OAM field in each refresh cycle during Low Power     |
27 | Idle. The sixteen OAM symbols are consecutively inserted into sixteen    |
28 | consecutive RS-FEC frames and/or refresh cycles."                             |
29 |                                     |
30 | RATIONALE FOR REVISION:              |
31 | There is an incorrect reference to PHY frame instead of RS-FEC frame.     |
32 | If one symbol was placed in each PHY frame then an OAM symbol would      |
33 | only be transmitted every 16 RS-FEC frames. Each PHY frame contains 16    |
34 | 10-bit OAM fields, not just one.                                           |
35 |                                     |
36 | IMPACT ON EXISTING NETWORKS:         |
37 | This change will impact any 2.5GBASE-T1, 5GBASE-T1, or 10GBASE-T1 PHY    |
38 | implementations that insert only one OAM symbol per PHY frame.            |
39 |                                     |
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41 | |Please attach supporting material, if any |
42 | |Submit to:-   David Law, Chair IEEE 802.3 |
43 | |and copy:-    Adam Healey, Vice-Chair IEEE 802.3 |
44 | |             |
45 | |At:-         E-Mail: stds-802-3-maint-req@ieee.org |
46 | |             |
47 | |             +----- For official use -----+ |
48 | |             | REV REQ NUMBER: 1449 | |
49 | |             | DATE RECEIVED: 04 October 2024 | |
50 | |             | EDITORIAL/TECHNICAL | |
51 | |             | ACCEPTED/DENIED | |
52 | |             | BALLOT REQ'D   YES/NO | |
53 | |             | COMMENTS: | |
54 | +-----+
55 | | For information about this Revision Request see - |
56 | |http://www.ieee802.org/3/maint/requests/revision_history.html#REQ1449 |
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