

IEEE 802.3 Pin-Optimized PHY Interface Study Group Opening Report

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IEEE 802.3 Pin-Optimized PHY Interface Study Group

Study Group information

Study Group Organization

Jason Potterf, IEEE 802.3 Pin-Optimized PHY Interface Study Group Chair

TBD, IEEE 802.3 Pin-Optimized PHY Interface Study Group Secretary

Study Group charter

Move that the IEEE 802.3 Working Group request the formation of a Study Group to develop a Project Authorization Request (PAR) and Criteria for Standards Development (CSD) responses for an Ethernet Media Independent Interface (MII) optimized for an exposed interconnect.

Study Group web and reflector information

Reflector information: <https://www.ieee802.org/3/POPI/reflector.html>

Home page: <https://www.ieee802.org/3/POPI/index.html>

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Activities since March 2025 plenary

Study group held one electronic Interim meeting in June of 2025

Major items discussed, decisions made and actions

PAR and CSD responses were reviewed and the study group voted to submit them to the LMSC for consideration

Current status

PAR and CSD responses are complete and submitted to the 802 working groups for comment

POPI PAR Responses as approved by the study group

https://www.ieee802.org/3/POPI/P802.3dq_PAR_SG_Approved_2025-06-18.pdf

POPI Draft CSD Responses as approved by the study group

https://www.ieee802.org/3/POPI/POPI_DRAFT_CSD_2025-06-18_v02.pdf

POPI Draft Objectives

https://www.ieee802.org/3/POPI/POPI_DRAFT_Objectives_2025-06-18_v01.pdf

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Meeting week plan

Study Group will meet Wednesday in the PM1 slot to consider feedback from other working groups and vote on any changes prior to the LMSC deadline

Questions?

Thank you!